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initial begin: init1. A = 3; #1 A = A + 1; // blocking procedural assignment. B = A + 1; \$display ("Blocking: A= %b B= %b", A, B); A = 3; #1 A <= A + 1; // non-blocking procedural assignment. B <= A + 1; #1 \$display ("Non-blocking: A= %b B= %b", A, B); end. VERILOG INTERVIEW QUESTIONS-----Lumos.

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